





# 2<sup>nd</sup> Order PLL Design and Analysis

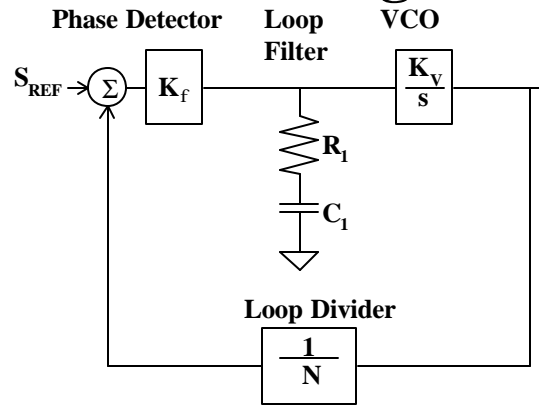


Fig. 1: 2<sup>nd</sup> Order PLL with Current-Mode Phase Detector

- ▣ useful functions and identities
- ▣ Units
- ▣ Constants

---

## Table of Contents

- I. Introduction
- II. Inputs
- III. Initial Calculations
- IV. Loop Filter Design Procedure
- V. 2<sup>nd</sup> Order PLL Design Function
- VI. Optimal Settling Time for 2<sup>nd</sup> Order PLLs
- VII. Outputs
- VIII. Noise, Transfer Function, and Settling Time Analysis
- IX. Small Signal Transfer Functions
- X. Phase Noise Calculations
- XI. Transient Step Response
- XII. Plots
- XIII. Copyright and Trademark Notice

## Introduction

Although the emphasis of PLL design is on 3<sup>rd</sup>-order PLLs, the settling time of second order PLLs is important, especially for boosted PLLs. In boost mode the transfer function of a third-order PLL sometimes reverts to a second order expression. As with 3<sup>rd</sup>-order PLL's there are different implementations, including voltage mode and those with operational-amplifier based phase detectors. This report will talk about the most popular, which uses a current-mode phase detector and a passive loop filter.

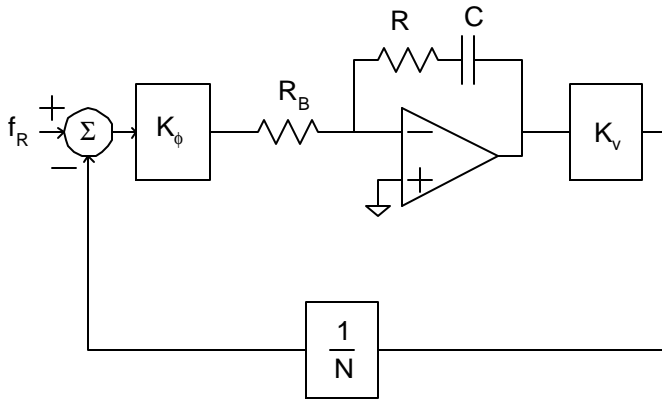


Fig. 2: 2<sup>nd</sup>-order PLL with voltage-mode phase detector and active filter

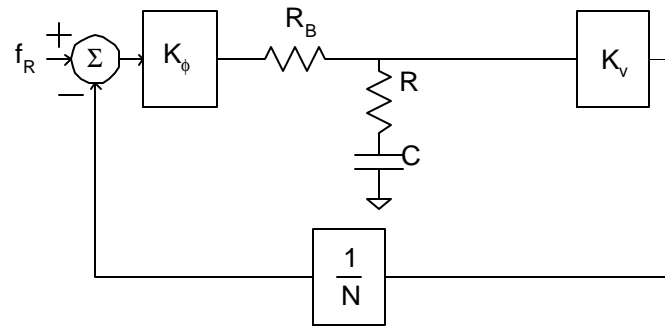


Fig. 3: 2<sup>nd</sup>-order PLL with voltage-mode phase detector and passive filter

## Inputs

$$f_r := 10 \cdot \text{MHz}$$

$$f_o := 1 \cdot \text{GHz}$$

$$f_{\text{acc}} := 1 \cdot \text{kHz}$$

$$f_{\text{step}} := 25 \cdot \text{MHz}$$

$$I := 50 \mu\text{A}$$

$$f_u := 1 \cdot \text{kHz}$$

$$K_V := 2 \cdot \pi \cdot 10 \cdot 10^6 \cdot \frac{\text{rad}}{\text{sec} \cdot \text{volt}}$$

$$\text{PM}_{\text{des}} := 70 \text{deg}$$

Reference oscillator frequency and period

Output frequency

Acceptable Frequency Error

Maximum Frequency Step (output referred)

Charge Pump Current

Unity Gain Frequency

VCO Gain

Phase Margin (70 degrees recommended, 50 degrees if slewing is considered)

## Initial Calculations

$$T := \frac{1}{f_r}$$

$$T = 0.1 \mu\text{s}$$

Reference Period

$$\omega_u := 2 \cdot \pi \cdot f_u$$

$$\omega_u = 6.283 \times 10^3 \frac{\text{rad}}{\text{sec}}$$

Unity Gain Frequency

$$N := \frac{f_o}{f_r}$$

$$N = 100$$

Loop divider ratio

$$K_\phi := \frac{I}{2 \cdot \pi}$$

$$K_\phi = 7.958 \frac{\mu\text{A}}{\text{rad}}$$

Phase Detector Gain

## Loop Filter Design Procedure

The open-loop transfer functions for the PLL are:

$$GH(s) = K_{\phi} \cdot \frac{R_1 \cdot C_1 \cdot s + 1}{C_1 \cdot s} \cdot \frac{K_v}{s} \cdot \frac{1}{N} = K_{\phi} \cdot \frac{\omega_z \cdot s + 1}{C_1 \cdot s} \cdot \frac{K_v}{s} \cdot \frac{1}{N}$$

The angle of open-loop gain is

$$\text{AngleGH}(\omega_u) = \text{atan}(\omega_u \cdot \omega_z) - 180\text{deg}$$

and the phase margin is thus given by

$$\text{PM}(\omega_u \cdot \omega_z) := \text{atan}(\omega_u \cdot \omega_z)$$

Use to solve for the zero/unity gain bandwidth spacing:  $\omega_u \cdot \omega_z$

$$\omega_u \cdot \omega_z := \tan(\text{PM}_{\text{des}})$$

and the zero location

$$\omega_z := \frac{\omega_u}{\omega_u \cdot \omega_z} \qquad \frac{\omega_z}{2 \cdot \pi} = 0.364 \text{ kHz}$$

The magnitude transfer function is given by:

$$\text{MagGH}(\omega_u) = 1 = \frac{\sqrt{\omega_u \cdot \omega_z^2 + 1}}{C_1 \cdot \omega_u} \cdot \frac{K_v}{\omega_u} \cdot \frac{K_{\phi}}{N}$$

and set this equal to one to find  $C_1$ , given  $\omega_u$ .

$$C_1 := \frac{\sqrt{\omega_u \cdot \omega_z^2 + 1}}{\omega_u} \cdot \frac{K_v}{\omega_u} \cdot \frac{K_{\phi}}{N} \qquad C_1 = 370.304 \text{ nF}$$

Use  $C_1$  and  $\omega_z$  to find  $R_1$ :

$$R_1 = \frac{1}{\omega_z \cdot C_1} \qquad R_1 := \frac{\omega_u \cdot \omega_z}{\sqrt{\omega_u \cdot \omega_z^2 + 1} \cdot \frac{K_v}{\omega_u} \cdot \frac{K_{\phi}}{N}} \qquad R_1 = 1.181 \text{ k}\Omega$$

Plugging these back into the transfer function we get: the following feedforward and feedback transfer functions.

$$G(s) = \frac{\omega_u \cdot \omega_z \cdot \frac{s}{\omega_u} + 1}{\sqrt{\omega_u \cdot \omega_z^2 + 1}} \cdot \frac{N}{\left(\frac{s}{\omega_u}\right)^2} \qquad H := \frac{1}{N}$$

For frequencies much greater than the unity gain bandwidth the magnitude response is

$$G(f) = K_{\phi} \cdot \frac{R_1 \cdot K_v}{2 \cdot \pi \cdot f} = \frac{\omega_u \cdot \omega_z}{\sqrt{\omega_u \cdot \omega_z^2 + 1}} \cdot \frac{N}{\left(\frac{f}{f_u}\right)} \qquad H = \frac{1}{N}$$

For  $\omega_u \cdot \omega_z^2 \gg 1$ , which is usually true:

$$G(f) = N \cdot \frac{f_u}{f} \qquad H = \frac{1}{N}$$

Note that second order PLL only gives a first order roll off, because the stabilizing zero in the loop.

## 2<sup>nd</sup>-Order PLL Design Function

The following function repeats the calculations above

$$\text{pll2nd}(f_r, f_o, I, f_u, K_v, \text{PM}) := \left( \begin{array}{l} \omega_u \cdot \omega_z \leftarrow \tan(\text{PM}) \\ N \leftarrow \frac{f_o}{f_r} \\ K_\phi \leftarrow \frac{I}{2 \cdot \pi \cdot \text{rad}} \\ \omega_u \leftarrow 2 \cdot \pi \cdot \text{rad} \cdot f_u \\ C_1 \leftarrow \frac{\sqrt{\omega_u \cdot \omega_z^2 + 1 \cdot K_\phi \cdot K_v}}{\omega_u^2 \cdot N} \\ R_1 \leftarrow \frac{\omega_u \cdot \omega_z}{\omega_u \cdot C_1} \\ \left( \begin{array}{l} \frac{C_1}{\text{farad}} \\ \frac{R_1}{\text{ohm}} \end{array} \right) \end{array} \right)$$

$$x := \text{pll2nd}(f_r, f_o, I, f_u, K_v, \text{PM}_{\text{des}})$$

$$C_1 := x_1 \cdot \text{farad} \quad C_1 = 370.304 \text{ nF}$$

$$R_1 := x_2 \cdot \text{ohm} \quad R_1 = 1.181 \text{ kohm}$$

Main Loop Filter Capacitor  
Main Loop Resistor

## Optimal Settling-Time for 2<sup>nd</sup> Order PLLs

The closed-loop transfer function for a second order PLL is:

$$A(s) = \frac{N \cdot \left( 1 + \omega_u \omega_z \frac{s}{\omega_u} \right)}{b \cdot \left( \frac{s}{\omega_u} \right)^2 + \omega_u \omega_z \frac{s}{\omega_u} + 1} \quad \text{where } b = (\omega_u \omega_z^2 + 1)^{\frac{1}{2}}$$

To simplify the transient response, we can assume  $\omega_u^2$  is much greater than  $\omega_z^2$ . In this case the closed loop transfer function simplifies to:

$$A(s) = \frac{N \cdot \left( 1 + \omega_u \omega_z \frac{s}{\omega_u} \right)}{\omega_u \omega_z \left( \frac{s}{\omega_u} \right)^2 + \omega_u \omega_z \left( \frac{s}{\omega_u} \right) + 1}$$

The simplified transfer function is acceptable for most situations, except when optimizing the bandwidth. It tends to underestimate the settling time for low phase margins. The inverse Laplace transform of the true transfer function is

$$f_{\text{err}}(\text{out}, \omega_u \omega_z) := \begin{cases} b \leftarrow \sqrt{\omega_u \omega_z^2 + 1} \\ c \leftarrow \sqrt{4 \cdot \sqrt{\omega_u \omega_z^2 + 1} - \omega_u \omega_z} \\ -\omega_u \cdot \exp \left[ \frac{-\omega_u \omega_z}{2} \cdot \left( -2 + 5^{\frac{1}{2}} \right) \cdot \text{out} \right] \cdot \frac{\text{out} - \omega_u \omega_z}{\left( \frac{\omega_u \omega_z}{2} \right)^2} & \text{if } \omega_u \omega_z = 2 \cdot \sqrt{2 + \sqrt{5}} \\ \left[ f_{\text{step}} \cdot \left[ \left( \frac{\omega_u \omega_z}{c} \cdot \sin \left( c \cdot \frac{\text{out}}{2 \cdot b} \right) - \cos \left( c \cdot \frac{\text{out}}{2 \cdot b} \right) \right) \cdot \exp \left( -\omega_u \omega_z \cdot \frac{\text{out}}{2 \cdot b} \right) \right] \right] & \text{if } \omega_u \omega_z \neq 2 \cdot \sqrt{2 + \sqrt{5}} \end{cases}$$

The inverse Laplace transform of the simplified transfer function is

$$f_{\text{err}2}(\text{out}, \omega_u \omega_z) := \begin{cases} y \leftarrow \sqrt{\frac{4}{\omega_u \omega_z} - 1} \\ f_{\text{step}} \cdot \left( \frac{\text{out}}{2} - 1 \right) \cdot \exp \left( \frac{-1}{2} \cdot \text{out} \right) & \text{if } \omega_u \omega_z = 4 \\ f_{\text{step}} \cdot \left[ \left( -\cos \left( \frac{1}{2} \cdot y \cdot \text{out} \right) + \frac{\sin \left( \frac{1}{2} \cdot y \cdot \text{out} \right)}{y} \right) \cdot \exp \left( \frac{-1}{2} \cdot \text{out} \right) \right] & \text{if } \omega_u \omega_z \neq 4 \end{cases}$$

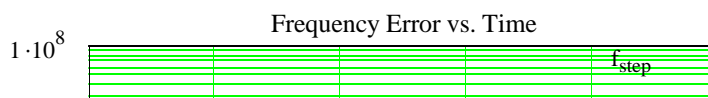
For comparison, we also plot an ideal settling with a bandwidth of  $\omega_u$ .

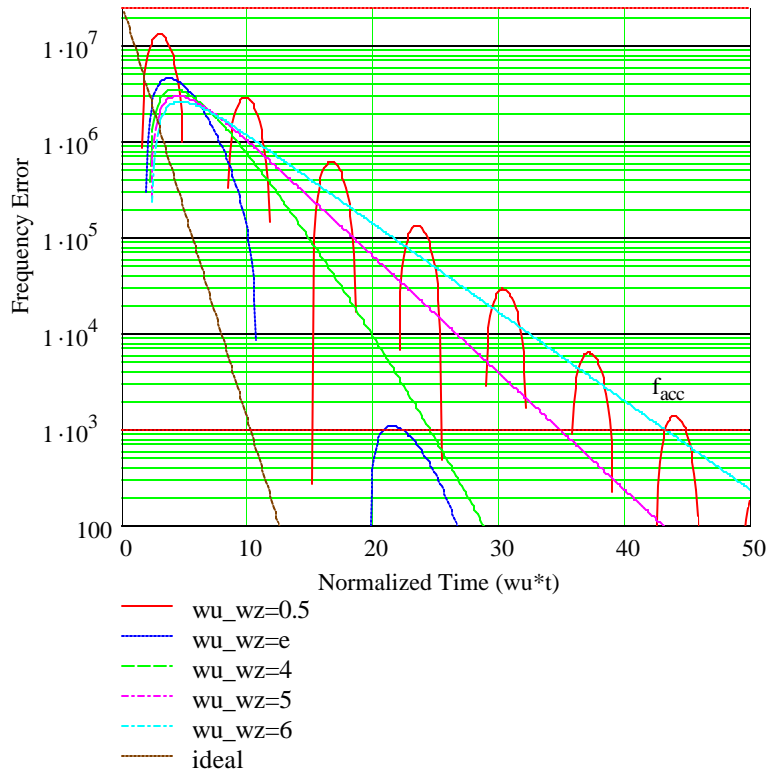
$$f_{\text{errideal}}(\text{out}) := f_{\text{step}} \cdot e^{-\text{out}}$$

A plot of this response is given below for different values of  $\omega_u \omega_z$ .

$$\text{num} := 400 \quad i := 1.. \text{num}$$

$$\text{out}_i := \frac{i}{\text{num}} \cdot 50$$





The following function calculates the settling time of the transient response

```

tsettle(wu_wz) :=
  out_val ← 30
  for i ∈ 2..num
    out_val ← if[ (|f_err(out_i, wu_wz)| ≤ f_acc) · (|f_err(out_{i-1}, wu_wz)| > f_acc), out_i, out_val ]
  out_val

```

A plot of the settling time vs phase margin is given below.

```

numB := 100      ωu_ωz_min := 1.5      ωu_ωz_max := 6

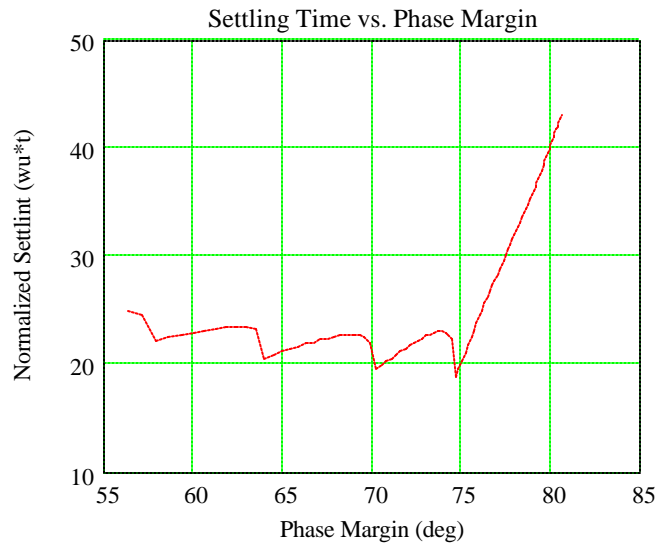
```

```

i := 1..numB

```

$$\omega_{u\omega z_{val}i} := \frac{i-1}{\text{numB}-1} \cdot (\omega_{u\omega z_{max}} - \omega_{u\omega z_{min}}) + \omega_{u\omega z_{min}}$$



The following function sweeps through values of wu\_wz to find the optimal value.

```

ωu_ωz_opt :=
  out_setmin ← 10^9
  ωu_ωz_min ← 0

```

```

ωωopt := ~
for i ∈ 1.. numB
  ωω ←  $\frac{i-1}{\text{numB}-1} \cdot (\omega_{\omega\text{max}} - \omega_{\omega\text{min}}) + \omega_{\omega\text{min}}$ 
  outsettle ←  $\left[ \begin{array}{l} \text{out}_{\text{val}} \leftarrow 10^9 \text{ sec} \\ \text{for } i \in 2.. \text{num} \\ \text{out}_{\text{val}} \leftarrow \text{if} \left[ (|f_{\text{err}}(\text{out}_i, \omega_{\omega})| \leq f_{\text{acc}}) \cdot (|f_{\text{err}}(\text{out}_{i-1}, \omega_{\omega})| > f_{\text{acc}}), \text{out}_i, \text{out}_{\text{val}} \right] \\ \text{out}_{\text{val}} \end{array} \right]$ 
  ωωopt ← if(outsettle < outsetmin, ωω, ωωopt)
  outsetmin ← if(outsettle < outsetmin, outsettle, outsetmin)
ωωopt

```

$\omega_{\omega\text{opt}} = 3.636$

This corresponds to an optimum phase margin of

$\text{PM}_{\text{opt}} := \text{PM}(\omega_{\omega\text{opt}})$

$\text{PM}_{\text{opt}} = 74.624 \text{ deg}$

We decrease this phase margin slightly to account for loop gain variations. The optimal settling time is

$t_{\text{settleopt}} := t_{\text{settle}}(\omega_{\omega\text{opt}})$

$t_{\text{settleopt}} = 18.75$

## Outputs

$C_1 = 370.304 \text{ nF}$

$R_1 = 1.181 \text{ kohm}$

Main Loop Filter Capacitor

Main Loop Resistor



## Example Noise Parameters

Using typical phase noise values, we can plot the phase noise of the overall loop.

$$L_{VCO} := -110 \text{dBc\_Hz}$$

VCO Phase Noise at  $f_{VCO}$

$$f_{VCO} := 100 \text{kHz}$$

Frequency for VCO Phase Noise

$$L_{pdet} := -120 \text{dBc\_Hz}$$

Reference Phase Noise at  $f_{pdet}$

$$f_{pdet} := 100 \text{kHz}$$

Frequency for Phase Detector Phase Noise

$$f_{start} := \frac{f_u}{10} \quad f_{stop} := f_u \cdot 1000$$

Start and Stop Frequencies for Plotting

Measured Lref data from <http://www.rakon.com/VTXO100spec.html>

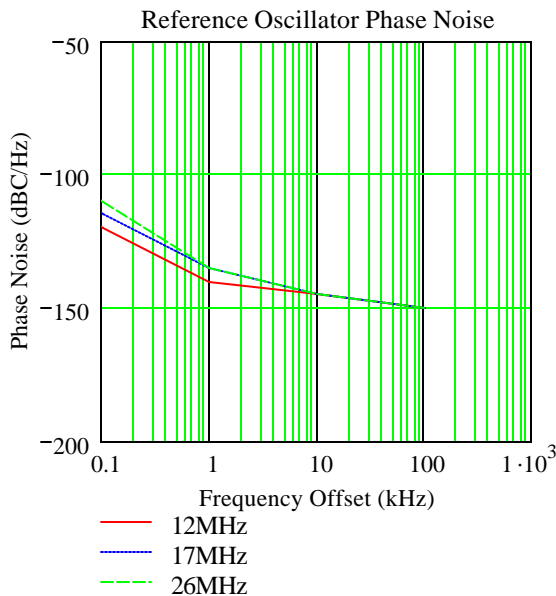
$$f_{refMHz} := \begin{pmatrix} 1 \\ 10 \\ 100 \\ 1000 \\ 10^4 \\ 10^5 \end{pmatrix} \text{Hz} \quad L_{ref12MHz} := \begin{pmatrix} -60 \\ -90 \\ -120 \\ -140 \\ -145 \\ -150 \end{pmatrix} \text{dBc\_Hz} \quad L_{ref17MHz} := \begin{pmatrix} -55 \\ -85 \\ -115 \\ -135 \\ -145 \\ -150 \end{pmatrix} \text{dBc\_Hz} \quad L_{ref26MHz} := \begin{pmatrix} -50 \\ -80 \\ -110 \\ -135 \\ -145 \\ -150 \end{pmatrix} \text{dBc\_Hz}$$

$$i := 1..num \quad f_i := f_{start} \cdot \left( \frac{f_{stop}}{f_{start}} \right)^{\frac{i}{num}}$$

Logarithmic Frequency Vector

$$L_{ref_i} := \text{linterp} \left( \log \left( \frac{f_{refMHz}}{\text{Hz}} \right), L_{ref12MHz}, \log \left( \frac{f_i}{\text{Hz}} \right) \right)$$

Log-Linear Interpolated Reference Phase Noise Vector



## Small Signal Transfer Functions

$$\omega_i := 2 \cdot \pi \cdot f_i \quad s_i := j \cdot \omega_i$$

The loop's feedforward and feedback transfer functions are

$$G_i := \frac{K_\phi \cdot (1 + R_1 \cdot C_1 \cdot s_i) \cdot K_v}{C_1 \cdot s_i \cdot s_i} \quad \text{Open Loop Feedforward Gain}$$

$$H := \frac{1}{N} \quad \text{Open Feedback Gain}$$

When the loop is modulated, it is useful to know the group delay of the PLL as a function of frequency.

$$\text{gdhp}(f) := \frac{d}{df} \arg \left[ \frac{1}{1 + \frac{K_\phi \cdot (1 + R_1 \cdot C_1 \cdot j \cdot 2 \cdot \pi \cdot f) \cdot K_v \cdot \frac{1}{N}}{C_1 \cdot j \cdot 2 \cdot \pi \cdot f}} \right] \quad \text{High Pass Group Delay}$$

$$\text{gdlp}(f) := \frac{d}{df} \arg \left[ \frac{\frac{K_\phi \cdot (1 + R_1 \cdot C_1 \cdot j \cdot 2 \cdot \pi \cdot f) \cdot K_v}{C_1 \cdot j \cdot 2 \cdot \pi \cdot f}}{1 + \frac{K_\phi \cdot (1 + R_1 \cdot C_1 \cdot j \cdot 2 \cdot \pi \cdot f) \cdot K_v \cdot \frac{1}{N}}{C_1 \cdot j \cdot 2 \cdot \pi \cdot f}} \right] \quad \text{Low Pass Group Delay}$$

## Phase Noise Calculations

The feedforward noise transfer function for  $R_1$  to the output is:

$$G_{R1_i} := 1$$

The closed loop noise transfer function for  $R_1$  to the output is:

$$L_{R1_i} := \left[ 4 \cdot k \cdot \text{Temp} \cdot R_1 \cdot (|G_{R1_i}|)^2 \right] \cdot \left[ \left| \frac{K_v}{(1 + G_i \cdot H) \cdot s_i} \right| \right]^2$$

The phase noise spectrum of VCO to the output is:

$$L_{VCO_i} := \left( \frac{L_{vco} - 20 \cdot \log \left( \frac{f_i}{f_{vco}} \right)}{10} \right) \cdot \left( \left| \frac{1}{1 + G_i \cdot H} \right| \right)^2$$

The phase noise spectrum of reference oscillator to the output is:

$$L_{REF_i} := \frac{L_{ref_i}}{10} \cdot \left( \left| \frac{G_i}{1 + G_i \cdot H} \right| \right)^2$$

Phase noise spectrum of phase detector to the output is:

$$L_{PDET_i} := \frac{L_{pdet} - 10 \cdot \log \left( \frac{f_i}{f_{pdet}} \right)}{10} \cdot \left( \left| \frac{G_i}{1 + G_i \cdot H} \right| \right)^2$$

The total output phase noise spectrum is

$$L_{tot_i} := 10 \cdot \log \left[ (L_{R1_i} + L_{VCO_i} + L_{PDET_i} + L_{REF_i}) \cdot \text{Hz} \right]$$

## Transient Step Response

Here two transient responses for the PLL are plotted, one with the effects of slewing considered and one without. The maximum slew-rate for the output frequency is limited by the rate at which the charge-pump can charge the loop filter capacitor.

$$\Delta f_{\Delta t_{\max}} := \frac{I}{C_1} \cdot \frac{K_v}{2 \cdot \pi} \qquad \Delta f_{\Delta t_{\max}} = 1.35 \frac{\text{MHz}}{\text{mS}}$$

A pure slewing step response is given by the following expression:

$$h_{\text{slew}}(t) := \Delta f_{\Delta t_{\max}} \cdot t$$

Time to slew to desired frequency

$$t_{\text{slew}} := \frac{f_{\text{step}}}{\Delta f_{\Delta t_{\max}}} \qquad t_{\text{slew}} = 18.515 \text{ mS}$$

The slewing expression is interesting to see when all the variables have been substituted. Here we see that increasing the bandwidth (fractional-N), reducing the frequency step size (with switch-cap tuning), and higher phase margins can improve the slew rate, and increasing the output frequency (which also impacts  $f_{\text{step}}$ ). In general slewing improvement requires topology changes, not just a resizing.

$$t_{\text{slew}} = \frac{f_{\text{step}} \cdot \omega_u \cdot \omega_z}{N \cdot \omega_u^2}$$

A pure linear step response and frequency error is given by the following expressions:

$$h(t) := f_{\text{step}} + f_{\text{err}}(\omega_u \cdot t, \omega_u \cdot \omega_z)$$

$$f_{\text{linerr}}(t) := f_{\text{step}} - h(t)$$

To combine the linear and slewing responses, we find the time where the slopes are equal:

$$t_{\text{lin}} := \begin{cases} t \leftarrow \frac{\pi}{\omega_u} \\ \text{root} \left( \frac{d}{dt} h(t) - \Delta f_{\Delta t_{\max}}, t \right) \end{cases} \qquad t_{\text{lin}} = 0.562 \text{ mS} \qquad \text{Time where linear slewing starts}$$

Time where slewing stops

$$t_{\text{slew}} := \frac{h(t_{\text{lin}})}{\Delta f_{\Delta t_{\max}}} \qquad t_{\text{slew}} = 21.817 \text{ mS}$$

The linear + slewing step response is given as:

$$h_{\text{lin+slew}}(t) := \text{if}(t < t_{\text{slew}}, h_{\text{slew}}(t), h(t - t_{\text{slew}} + t_{\text{lin}}))$$

with a frequency error of

$$f_{\text{lin+slewe}}(t) := f_{\text{step}} - h_{\text{lin+slew}}(t)$$

The overall settling time is then calculated

$$\text{num} := 1000 \qquad i := 1.. \text{num}$$

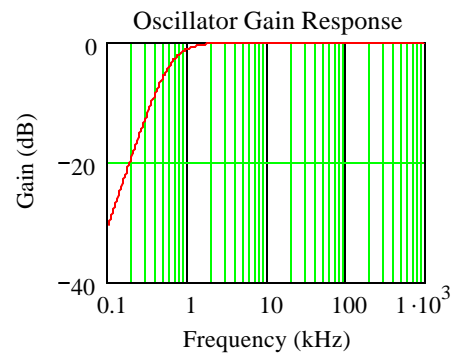
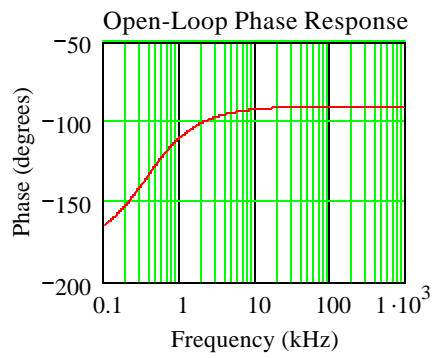
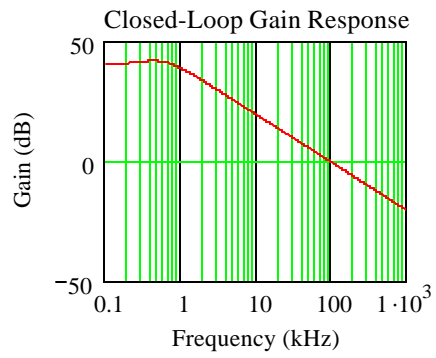
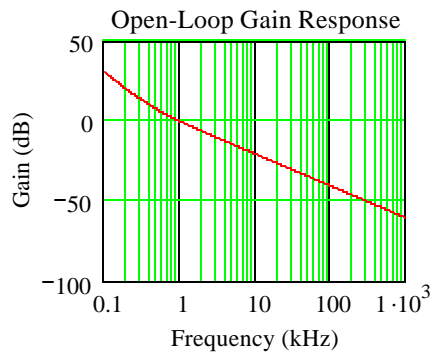
$$t_i := \frac{i}{\text{num}} \cdot \left( \frac{50}{\omega_u} + t_{\text{slew}} \right) \qquad \text{Time Vector}$$

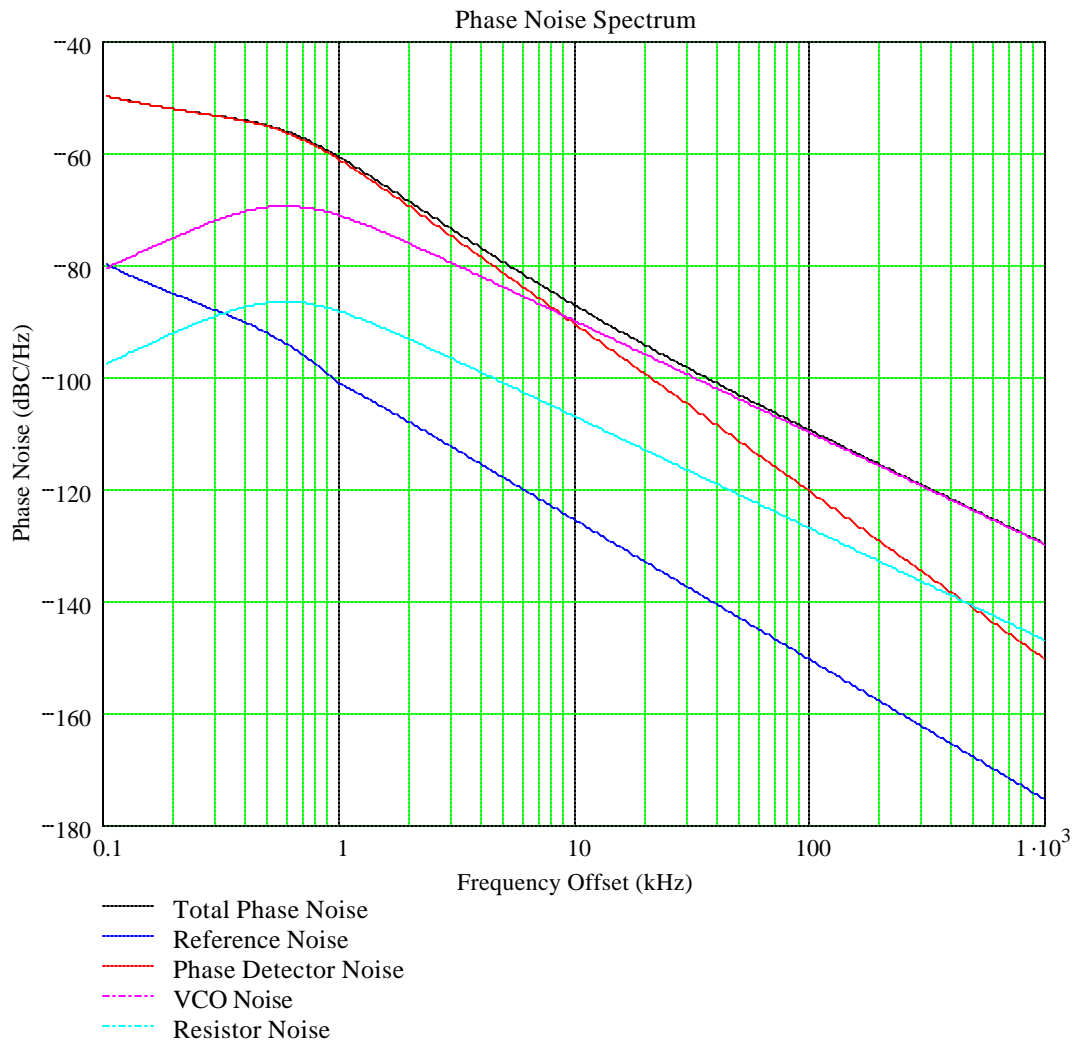
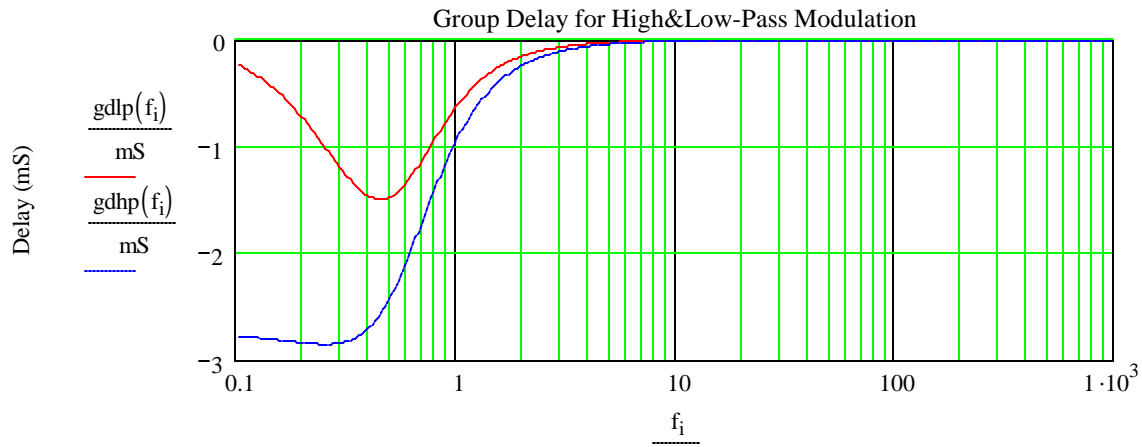
$$t_{\text{settle}} := \begin{cases} t_{\text{val}} \leftarrow 0 \text{sec} \\ \text{for } i \in 2.. \text{num} \\ t_{\text{val}} \leftarrow \text{if} \left[ \left( |f_{\text{lin+slewe}}(t_i)| \leq f_{\text{acc}} \right) \cdot \left( |f_{\text{lin+slewe}}(t_{i-1})| > f_{\text{acc}} \right), t_i, t_{\text{val}} \right] \\ t_{\text{val}} \end{cases}$$

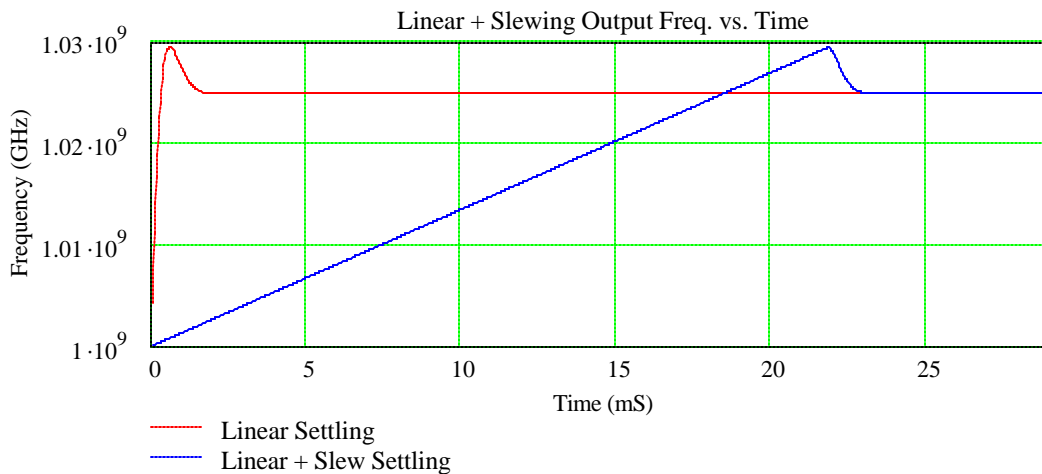
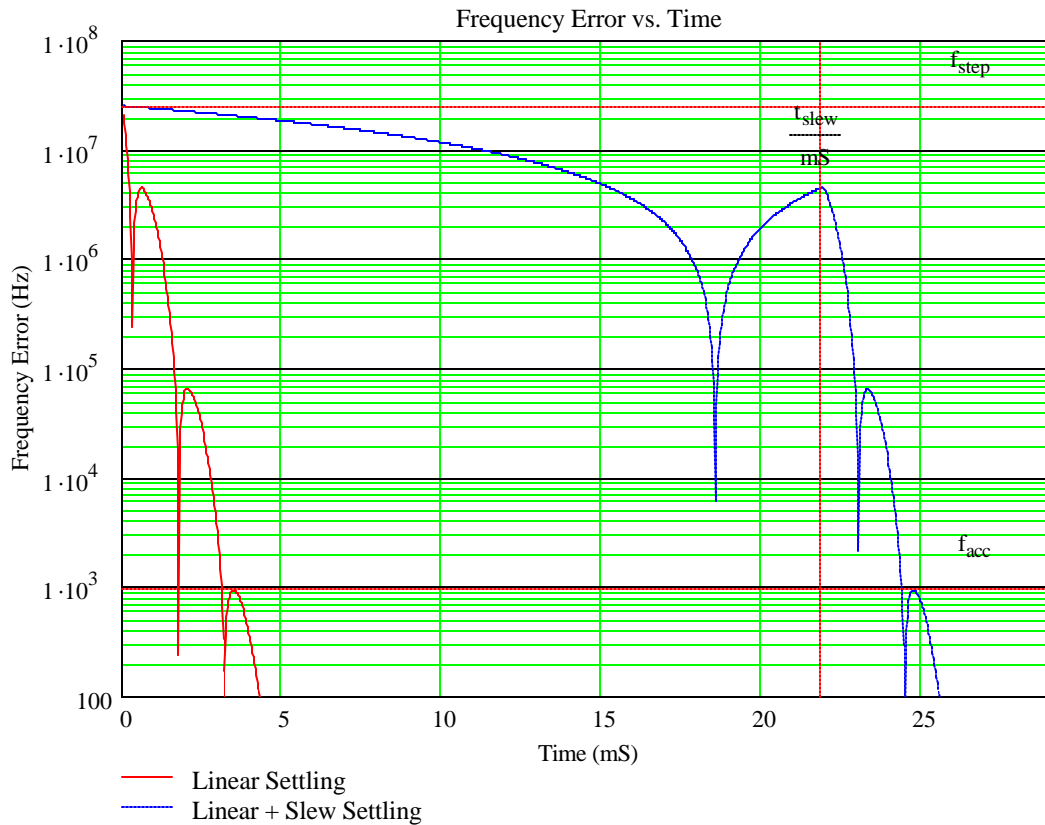
$$t_{\text{settle}} = 24.326 \text{ mS}$$

Total Settling Time

## Plots







## Copyright and Trademark Notice

All software and other materials included in this document are protected by copyright, and are owned or controlled by Circuit Sage.

The routines are protected by copyright as a collective work and/or compilation, pursuant to federal copyright laws, international conventions, and other copyright laws. Any reproduction, modification, publication, transmission, transfer, sale, distribution, performance, display or exploitation of any of the routines, whether in whole or in part, without the express written permission of Circuit Sage is prohibited.